



Applicant(s): David G. MIKOLAS et al.

Serial No.: 10/810,257

Filing Date: 26-Mar-04

For: DIELECTRIC-IN-DIELECTRIC

DAMASCENE PROCESS FOR MANUFACTURING PLANAR

WAVEGUIDES

CERTIFICATE OF MAILING

I, MARIEJOSE MONSALVE, HEREBY CERTIFY THAT THIS CORRESPONDENCE AND ENCLOSURE(S), IF ANY, ARE HEREBY BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE IN AN ENVELOPE WITH SUFFICIENT POSTAGE FOR FIRST CLASS MAIL DELIVERY TO THE USPTO AT THE MAIL STOP/ADDRESS INDICATED BELOW ON 13 Oct 04

(Depositor's signature)

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA, 22313-1450

Pursuant to 37 CFR 1.97 and 1.98, the references listed on the enclosed Form PTO/SB/08A are submitted for consideration by the Examiner in the examination of the above-identified patent application.

The cited document(s) disclose(s) numerous specific features. There has been no attempt to enumerate each and every feature disclosed by each document, however. The Examiner is requested to review the(se) document(s) and to determine the extent of the materiality of the document disclosure(s), if any, with respect to the present invention.

No inference should be drawn that the attached list represents a comprehensive investigation, or that any disclosure in the listed documents is equivalent to the subject invention. The discussion of any art and the citation of any document(s) herein is not to be construed as an admission that the art or document disclosure(s) is/are necessarily within the invention field of endeavor or is/are necessarily prior in time to a particular date which may be relevant with respect to the instant application. Further, the recitation herein of the art and document(s) is not to be construed as an assertion that more pertinent art could not possibly be in existence, or that the newly cited document(s) is/are necessarily prior art as defined by the patent law.

Applicant(s) reserve(s) the right to later set forth how the claimed invention is distinguished over the disclosure of any document or other art, including the disclosure(s) of the art and document(s) recited herein, and that may be cited by the Examiner in rejecting a claim in the instant patent application.

Cabot Microelectronics Corporation 870 North Commons Drive Aurora, Illinois, 60504 (630) 375-5465

Supplemental Information Disclosure Statement USSN 10/810,257

The In	formation Disclosure Statement is being filed:		
	within any one of the following time periods: (a) within three months of the filing date of a national application other than a continued prosecution application under 37 CFR 1.53(d); (b) within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 of an international application; (c) before the mailing date of a first Office Action on the merits; or (d) before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.		
X	after (a), (b), (c) or (d) above, but before the mailing date of a final action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an action that otherwise closes prosecution in the application, and includes <i>one</i> of:		
	the Statement under 37 CFR 1.97(e) (see "Statement under 37 CFR 1.97(e)" below).		
	or ☐ the fee of \$180 set forth in 37 CFR 1.17(p) (see "Fees" below).		
Staten	nent under 37 CFR 1.97(e)		
×	The undersigned hereby states that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign patent application not more than three months prior to the filing of the Information Disclosure Statement.		
Fees a	nd Method of Payment		
X X	No fee is owed by the applicant(s). Charge\$180 (IDS Fee due under 37 CFR 1.17(p)) to Deposit Account No. 501599. If any additional fees are owed in connection with this communication, please charge Deposit Account No. 501599.		
	Respectfully submitted,		
Date: _	130ct 04 Steven Weseman		

Attorney for the Applicant(s) Registration Number 41,372

Cabot Microelectronics Corporation 870 North Commons Drive Aurora, Illinois, 60504 (630) 375-5465

Comparable to Rorm PTO/SB/08A OCT 1 8 2004 1 INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Complete	e if Known
	Application Number:	10/810,257
	Filing Date:	26-Mar-04
	First Named Inventor::	David G. MIKOLAS
	Art Unit:	
	Examiner Name:	
	Atty. Docket No.:	100049

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.	Document Number Number-Kind Code (if known)	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages Or Figures Appear
	B1	5987196	16-Nov-1999	Noble	
	B2	2002/0106175	08-Aug-2002	Binkley et al.	
		-			

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No.	Foreign Patent Document Country Code- Number-Kind Code (if known)	Publication ⁻ Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages Or Figures Appear	Т
	В3	DE 10015830	11-Oct-2002	Fraunhofer Ges Forschung		

Examiner's Signature:	Date Considered: